Avnet Design Services is the technical arm of Avnet Electronics Marketing. Our mission is to accelerate our customers' successful time to market by advocating and implementing leading-edge semiconductor solutions for every stage of the design cycle. We offer highly trained and objective Field Applications Engineers (FAEs); FPGA and ASIC design centers; reference design kits and evaluation platforms and tools; and access to supplier technology, IP, training, and third-party technical alliances.

**Avnet Avenue - Design Centers**

FPGA design services are supported through a three-tiered support approach. Our local dedicated FAEs provide immediate, on-site support. Our regional Express FPGA Design Services team provides short term project design support from centralized locations. The Express team focuses on the optimization of existing designs, conversions from older or competitive architectures or file formats, and the integration and design of functional blocks that require two weeks or less of engineering effort. Our design center provides long term design support. Design Center consulting engagements are fully program-managed, contract-driven, and can take you all the way from concept-to-production or anywhere in between.

ASIC design services are provided from four ASIC centers employing over 20 engineers. Avnet Design Services has a 19-year ASIC history, with over 800 designs completed, and over 200 years combined experience. Our design centers provide RTL development, IP integration, text insertion, and layout services for deep submicron technologies.

**Avnet Avenue - Solutions**

Avnet's component offering is complimented by hardware, software, and service solutions that allow design engineers to improve their time to market. Avnet Avenue Solutions provides designers with access to over 25 Avnet-developed hardware kits, industry-leading software solutions, and customized design services.

Avnet's Hardware Kits allow designers to mix and match processor, FPGA, memory, networking, audio, video, mass storage, bus interface, high-speed serial interface, and wireless hardware boards to build their own system. Compatible software, firmware, IP, drivers and EDA tools from Avnet’s tool partners can further accelerate the development cycle. Avnet also offers superior component, board and system level design services through its partnership with the top design services companies in North America.

**Avnet Avenue - Design Support Services**

Our experienced Field Applications Engineers are extensions of our suppliers’ engineering staff. They provide application, technology and product expertise. FAEs are certified by suppliers and continuously trained on new technologies, including new product roadmaps and applications updates. FAEs are cross-trained in all the technologies on the Avnet Cilicon line-card, making them semiconductor specialists. The FAE teams leverage the training by multiple suppliers to provide system level solutions for our customers. They create optimized semiconductor solutions by comparing and contrasting multiple technology approaches. In addition to in-depth supplier knowledge, our FAEs also provide access to a suite of services and solutions available exclusively through Avnet Design Services.

**SpeedWay™ Workshop Series**

The SpeedWay Workshop Series is a curriculum offered to our North American Customers and given by trained Avnet Cilicon Field Application Engineers (FAE). Unlike any other workshop you have attended, these hardware-based, laboratory-oriented workshops put you in the middle of the action. With SpeedWay, you work with real hardware, real development tools, and build real sample designs. You will finish the workshop with an understanding of the advantages and disadvantages of various solutions, and an in depth knowledge of the product or technology illustrated in each lab.
Wavelength Design Solutions provides products and services which offer a total solution to our customers in the manufacturing, enterprise, medical, automotive and consumer markets that incorporate wireless technology into their end products.

**Development Tool Providers**

Celoxica
Avnet has partnered with Celoxica to distribute the DK Design Suite. The Celoxica DK Design Suite provides a design environment that brings together the flexibility of higher-level language design techniques and the capabilities of a powerful system-level co-design, co-verification and implementation environment.

**Mentor Graphics**
Avnet and Mentor Graphics have partnered through Mentor Graphics’ FPGA Advantage Solutions Thrust (FAST) Partner Program. Through the FAST Partner Program, Avnet Cilicon serves as a certified Mentor Graphics design resource to customers implementing FPGA design. The FAST Partner Program equips Avnet with Mentor's FPGA Advantage® tool suite, the only integrated FPGA design flow for design creation, debug, simulation and synthesis.

**Synopsys**
Synopsys, the technology leader for complex integrated circuit (IC) design, and Avnet have an agreed to jointly promote Synopsys FPGA Compiler II for synthesis of today’s complex FPGA designs. The Synopsys tool is now included as part of the Avnet Avenue Solution, allowing Avnet customers to benefit from easier access to the advanced features of FPGA Compiler II, including block-level incremental synthesis (BLIS) and retiming ultimately providing designers with the high-quality, fast synthesis designers require.

**Synplicity**
Synplicity, the leading provider of FPGA synthesis tools, and Avnet have partnered to promote complete solutions for Avnet FPGA and ASIC customers. The technology exchange between Avnet and Synplicity includes Synplicity authorization of Avnet’s ASIC and FPGA design centers and Structured ASIC Development Kit collaboration. Avnet Cilicon’s field representatives will also promote the complete set of Synplicity products to Avnet customers.

**Wind River**
Avnet and Wind River have partnered through the Wind River Partner Program. Wind River is the worldwide leader in embedded software and services. Wind River provides market-specific embedded platforms that integrate real-time operating systems, development tools and technologies.

**IP Core Providers**

**CAST**
Avnet has formed an alliance with CAST Inc., a provider of intellectual property (IP) cores, to jointly deliver CAST Xilinx Cores. CAST cores are offered in conjunction with Avnet Design Services designed hardware platforms, providing customers with complete development platforms and reference designs. This combination of proven hardware and IP will simplify a designer’s selection and evaluation process and remove risks, speeding time to market.

**Denali**
Avnet Design Services, in partnership with Denali, has created reference designs and IP Cores for selected Memory Processors. The newest product from Avnet Design Services that features a Denali, Databahn™-generated memory controller is the RLDDRAM Daughtercard.

**Service Providers**
Avnet Design Services has teamed with the top design service companies in North America to provide customers with superior board and system level solutions. In cooperation with Avnet Design Services, customers can access these pre-screened and certified design service providers.

- Formation
- Logic Product Development
- Paragon Innovations
- SET Engineering
- SMTC Design
- Sunrise Labs
- Videon Central

**Wavelength Design Solutions**
Wavelength Design Solutions provides products and services which offer a total solution to our customers in the manufacturing, enterprise, medical, automotive and consumer markets that incorporate wireless technology into their end products.

- **Wireless Technology**
  - WPAN
    - Short Range Transceivers
    - Bluetooth
    - Zigbee
  - WLAN
    - 802.11 b/a/g
    - Proprietary Transceivers (900 MHz, 2400 MHz)
  - WWAN
    - CDMA
    - GPS
    - GSM/GPRS
  - WMAN
    - 802.16

- **Product Offerings**
  - Wireless chipsets
  - Wireless modules
  - Development kits
  - Reference design kits
  - SDKs (binary & source)

- **Service Offerings**
  - Market analysis
  - Technology selection
  - Hardware development (RF, ASIC, PCB)
  - Software development (drivers, firmware, port)
  - Embedded expertise (CPU & RTOS)
  - Standards certification (Wi-Fi, Bluetooth)
  - Antenna selection
  - Impedance matching
  - Regulatory certification (FCC, CE)
**IBM 440 Evaluation Kit**

- 533MHz 440GX (upgradable to 800MHz)
- 128 MB SODIMM w/ECC DDR PC2700 module
- 8 MB FLASH memory (x16) expandable to 32 MB (x32)
- 64-bit PCI/PCI-X interface (33/66/133MHz)
- Ethernet Ports: 2 x 10/100/1000
- I2C Serial PROM (256k)
- RS-232 port (DB9)
- Second RS-232 port and 2 x I2C ports available on header
- Two AvBus connectors (expansion via 440GX External Peripheral Bus)
- Linux v2.4.xx

**IBM 405 Evaluation Kit**

- PPC405EP
- 4 MB FLASH
- 16 kb EEPROM
- 32 MB NAND FLASH
- 32 MB SDRAM
- 10/100 Ethernet
- 3, 5V PCI connectors
- Linux

**Texas Instruments Analog Adapter Kit**

- Two serial data converter sites or one dual-serial site
- One parallel data converter site
- On-board power supply for data converter and signal conditioning EVMs
- Two signal conditioning sites
- AvBus standard evaluation board
- One TMS320 compatible daughtercard site

**Texas Instruments USB Adapter Kit**

- Direct interface to 5K, 6K and most 2K DSK boards
- USB 2.0 (Cypress CY7C68001)
- Host and embedded demonstration software
- DSK to AvBus capable

**Motorola ColdFire® Evaluation Kit**

- Motorola MCF5282 ColdFire® processor
- 16 MB SDRAM
- 8 MB FLASH
- CAN interface
- RS-232 serial ports (2)
- 10/100 Mbps ethernet
- BDM/JTAG connector
- 8-channel, 10-bit queued analog-to-digital converter (QADC)
- Queued serial peripheral interface (QSPI) port
- Expansion (AvBus)

**Xilinx Spartan™-3 Evaluation Kit**

- Xilinx XC3S400-FG456 or XC3S1500-FG456 Spartan-3 FPGA
- Xilinx platform FLASH configuration PROM
- 2 Oscillators (66 MHz installed & socket for use frequency selection)
- Parallel cable III equivalent JTAG configuration port
- 2 AvBus expansion connectors
- 1, 50-pin header for easy I/O access (includes 4 LVDS pairs)
- Universal 32-bit PCI edge connector*
- 10/100 Ethernet port*
- DB15 & video DAC
- RS-232 console
- PS2 keyboard and mouse ports
- Analog I/O**
- 1M SRAM
- 256kb serial EEPROM
- 4-position DIP switch
- 2 push-buttons
- 8 discrete LEDs
- Dual-digit 7-segment LED display
* Development using these ports will require purchase or creation of appropriate IP cores
** Analog I/O provided as described in Xilinx Application Notes XAPP154 & XAPP155
**Development Kits**

**Xilinx Virtex™-II Development Kit**
- Xilinx XC2V1500, 4000 or 6000 FPGA
- JTAG connectors support MultiLYNX™, Parallel III and Parallel IV JTAG cables
- Xilinx XCCACEMxx-BG388I System ACE MPM solution
- 8 MB FLASH
- JTAG port

**Xilinx Virtex™-E Development Kit**
- Xilinx XC1000E-6FG1156C
- 64 MB SDRAM
- 32 MB FLASH
- Video input/output
- Audio output
- 10/100 ethernet
- CAN
- USB 2.0
- AvBus expansion connectors
- PCI 1.1 connector
- PCI Mezzanine card connectors
- PCI bus or single 7V-9V external supply
- RS-232 serial port
- Video color bars
- LED patterns
- PC card socket
- Universal connector 3.3V/5V, 32/64 bit for PCI/PCI-X interface
- RS-232 serial port
- 140-pin I/O expansion connectors (AvBus)
- 133 MHz, 128 MB DDR SDRAM DIMM
- Support for Versatile I/O and LVDS
- 8 DIP switches

**Xilinx Virtex-II Pro™ Development Kit**
- FPGA
- Xilinx Virtex-II Pro™ XC2VP7-xFF896 or XC2VP20-xFF896
- High-speed serial communication
  - Two HSSDC2 connectors (InfiniBand, user may replace with Fibre Channel)
  - Pads for an XPAK module (10Gb Ethernet, OC-192)
  - Receptacles for 2 SFP modules (Gigabit Ethernet, Fibre Channel, InfiniBand)
- Board I/O connectors
  - 32-bit PCI Mezzanine Card (PMC) connectors
  - Four 140-pin general purpose I/O expansion connectors (AvBus)
- Memory
  - Micron DDR SDRAM SODIMM (128 MB expandable to 1GB)
  - Micron Mobile SDRAM (two 8 MB x 16 devices, 32 MB total)
  - Cypress asynchronous SRAM (512Kbit x 32, 2 MB total)
  - Intel StratFlash® (16 MB total)
  - Compact FLASH card
- PCI
  - PCI bridge - Xilinx Spartan™-III
  - XC2S300E-FG456 FPGA
  - Configuration or file transfer & system control
  - Universal PCI connector (32-bit or 64-bit slot compatible)
  - Support for both 3.3V and 5.0V PCI signaling
  - Windows based GUI interface
- Power
  - Power supply daughter card (+3.3V and +2.5V rails @ 14A total)
  - 50 Watt AC/DC +5.0V power supply
  - National linear regulators
- Configuration
  - Bridge (Spartan-III)
  - - Xilinx PROM XC18V02-VQ44
  - - In-system programmable PROM
  - - Target (Virtex-II Pro)
  - - Xilinx System ACE™ CF
  - - FLASH or SRAM via PCI and Windows application
- Communication
  - National 10/100/1000 MBit/s Ethernet PHY
  - RS-232 serial ports

**Evaluation Kits**

**Motorola 68HC908GP32 Evaluation Kit**
- Motorola MC68HC08 8-bit FLASH microcontroller
- 1x16 character LCD display
- Temperature sensor
- Onboard 5V voltage regulator
- RS-232 serial port

**Motorola 68HC908GP32 emWare® Enabled Evaluation Kit**
- Motorola 68HC908GP32 8-bit FLASH microcontroller
- 1x16 character LCD display
- Temperature sensor
- emGateway™ network bridge and management system
- EMITAIS™ client libraries for creating client applications
- 9V external power supply
- Onboard 5V voltage regulator
- emWare’s emMicro™ object server
- emNet™ lightweight device network protocol
- Piezoelectric speaker
- RS-232 serial port
Motorola PowerQUICC™ Evaluation Kit
- Motorola MPC857T PowerQUICC Communications Processor
- AvBus Expansion Connectors
- JTAG port
- 64 MB SDRAM
- 16 MB FLASH connectors (AvBus)
- 1 MB SRAM
- 4K bits serial EEPROM
- RS-232 serial port
- USB 1.1 port
- 10/100 Mbps Ethernet Port

Motorola M68HC08 Family Evaluation Kit, 908JL3 Version
- Motorola MC68HC908JL3 8-bit FLASH microcontroller
- Pressure sensor
- Temperature sensor
- Potentiometer adjustable analog input
- RS-232 serial port
- Optional piezoelectric speaker
- Optional 1x16 character LCD display

National Semiconductor Dual High-Current Power Supply Kit
- +1.3 to +3.5VDC programmable output
- Power good flag and output enable
- Power sequencing between the two supplies
- Dependent or independent operation
- Over-voltage protection
- External ON/OFF control
- Meets Xilinx FPGA power requirements
- Current limit protection
- LED output indicators
- Multiple input power plugs

Xilinx CoolRunner™-II Evaluation Kit
- Xilinx XC2C256-VQ100 CoolRunner-II CPLD
- Serial A/D converter
- Regulated 3.3V and 1.8V supply voltages generated from external 5V supply
- Header connectors for user access to CPLD I/O
- AvBus I/O expansion connector
- Dual-digit 7-segment LED display
- 4 DIP switches
- 1 push-button
- RS-232 serial port
- JTAG port

Xilinx PMC IRL™ Reference Design Kit
- Xilinx Virtex-II 2V1000-4FG456C/FF896C
- 32-bit, PMC Bus interface connector
- PowerPC connector
- JTAG connectors (2)
- SelectMap connector
- AvBus I/O connector
- 16 MB SDRAMs (2)
- QDR SRAMs (6)
- 2 MB parallel FLASH memory
- 4 MB serial FLASH memories (2)
- DIP switches, LEDs, push-buttons

Xilinx Spartan™-II Evaluation Kit
- Xilinx XC2S150-5PQ208 Spartan-II FPGA
- Xilinx XC18V01SO20C serial configuration PROM
- Regulated 3.3V and 2.5V supply voltages generated from external 5V supply
- Connectors for access to FPGA I/O, including two 50 pin 0.1 header connectors, three MICTOR connectors for easy logic analyzer connection and one 140 pin connector
- 40 MHz oscillator
- SPI 3-wire thermometer
- 8 LEDs
- 8 DIP switches
- 2 push-buttons
- RS-232 serial port
- JTAG port

Xilinx Spartan™-IIE Evaluation Kit
- Xilinx XC2S200E-6FT256C Spartan-IIE FPGA
- Xilinx XC18V02VQ44C serial PROM
- Selectable voltage regulators
- Multimedia interface
- 50 MHz oscillator
- AvBus expansion connectors
- LVDS interface
- Logic analyzer connectors (MICTOR)
- Dual-digit 7-segment LED display
- 8 LEDs
- 8 DIP switches
- 2 push-buttons
- RS-232 serial port
**Xilinx Virtex™-E Evaluation Kit**

- Xilinx XC1V1000-6PG256 Virtex-E FPGA
- Xilinx XC1V8010SO20C serial configuration PROM
- Regulated 3.3V and 1.8V supply voltages generated from external 5V supply
- Connectors for access to FPGA I/O, including two 50 pin 0.1 header connectors, pads for three MICTOR connectors for easy logic analyzer connection and pads for one 140 pin connector
- 40 MHz oscillator
- SPI 3-wire thermometer
- Dual-digit 7-segment LED display
- 8 LEDs
- 8 DIP switches
- 2 push-buttons
- RS-232 serial port
- JTAG port

**Xilinx Virtex™-II Evaluation Kit**

- Xilinx XC2V1000-4FG256 Virtex-II FPGA (256 fine pitch BGA)
- Xilinx XC1V804VQ44 configuration PROM
- Regulated +3.3 VDC and +1.5 VDC supplies generated from external +5.0 VDC source
- 40 MHz oscillator
- Demonstrations of RS-232 serial port, digital thermometer and LED pattern generator
- Connectors are provided for FPGA I/O, logic analyzer, and AvBus interfaces
- Dual-digit 7-segment LED display
- 8 LEDs
- 8 DIP switches
- 2 push-buttons
- 101 user I/O available
- JTAG port

**Xilinx Virtex™-II High-Speed Evaluation Kit**

- Xilinx XC2V40 Virtex-II FPGA
- Xilinx XC1V802 configuration PROM
- Regulated 3.3V, 2.5V and 1.5V supply voltages generated from external 5V supply
- FPGA configuration mode selected via DIP switch
- Connectors for access to FPGA I/O, including differential input and output pairs with selectable termination and I/O voltages
- 2 oscillator sockets
- 50 MHz oscillator
- Slave-serial configuration port
- Coin cell holder
- 1x16 character LCD display
- 4 LEDs
- 5 push-buttons
- JTAG port

**Xilinx Virtex™-II Pro™ Evaluation Kit**

- Xilinx Virtex-II Pro XC2V67-5(6)-FF896
- Eight SMA connectors (TX/RX pairs for two Rocket I/Os)
- Board configurable loop-back for two Rocket I/Os
- Pads for four additional Rocket I/Os
- Two 140-pin general purpose I/O expansion connectors (AvBus)
- Up to 30 LVDS pairs
- 50 pin 0.1" header
- Micron DDR SDRAM (64MB)
- RS-232 serial ports
- 22.5 Watt +5.0V/4.5A AC/DC power supply
- 3.3V/6A module
- National Linear regulators
- Two Xilinx XC1V804-VAQ44 PROMs
- Parallel IV cable support for JTAG
- Fly-wire support for Parallel-III and MultiLINX™

**Xilinx XPLA3 Evaluation Kit**

- Xilinx XCR3256XL CPLD
- Flexible power supply options including external regulated or unregulated supply or on board battery
- Flexible clocking options including on board 32.768 kHz oscillator or external clock with selectable termination
- JTAG ports with support of daisy chain operation
- Large prototyping area
- Two-digit LCD display
- Headers and solder pads for access to all CPLD I/O, clock and JTAG pins
- RS-232 serial port*
- Socket for 8 pin serial PROM*
- Test points for measuring CPLD power consumption independent of all other components
* Device layout exists on PCB but components are not populated

**SYSTEM SOLUTIONS**

**Xilinx and Cypress USB 2.0 to SCSI System Solution Kit**

- Xilinx Spartan-IIIE XC2S300E-PQ208
- Cypress CY7C68000 USB 2.0 Phy
- SCSI interface
- JTAG port
- 128 KB SRAM
- 128 KB FLASH
- 2-megabit configuration PROM
**Denali RLDRAM IP Core Demonstration Kit**
- Xilinx Virtex-II XC2V1000-6-FF896AFT0145
- Xilinx XC18V04VQ44C
- Easy to use command line user interface
- RS-232 serial port
- Two 140-pin general purpose I/O expansion connectors (AvBus)
- Two 8MB x 32 RLDRAMs (512Megabits total) (Infineon - HYB18RL25632AC-5)
- Memory bus speed of 400 MHz (200 MHz DDR)
- Pipelined queue based interface
- In-system programmable PROM
- Single port queued read/write interface
- Register interface
- Reset and initialization functions
- Self refresh and power down commands
- Supplied AC/DC +12V@3A power supply
- JTAG header connector
- 4 DIP switches
- 2 push-buttons

**Audio/Video Module**
The Audio/Video Module offers a platform to develop and test products that require audio and/or video interface.
- Philips SAA7113H video input processor
- Philips SAA7121H digital video encoder
- Philips UCB1400 stereo 20-bit audio CODEC
- Analog Devices ADV123 140 MHz triple video DAC
- Interface for OmniVision OV6630AA CMOS color digital camera
- Interface for Fujitsu MB86S02A CMOS color digital camera
- Interface for Sharp LQ057Q3DC02 color TFT LCD module
- PS/2 keyboard and mouse interfaces
- X/Y touchscreen controller
- AvBus expansion connector
- Downloadable VHDL source code for sample applications

**AvBus Adapter**
- Adapts male or female AvBus connectors to an interface cable
- Can also be utilized as a vertical height extension for other modules
- Adapter is unpopulated; AMP 5-179010-6 and 170031-6 connectors sold separately

**AvBus Breakout Module**
- Interfaces external signals to AvBus connectors
- Two male and two female AvBus connectors
- Six MICTOR logic pod interface connectors
- Four 50-pin 0.1” headers
- Power indicator LEDs for +5 VDC and +3.3 VDC

**Avnet Parallel Cable III**
- Parallel (printer) port connection to PC
- 6-foot length
- Keyed fly wires for JTAG (IEEE 1149.1)
- Keyed fly wires for Xilinx serial configurations
- Compatible with Xilinx ISE software

**Communications/Memory Module**
- 64 MB SDRAM
- 16 MB FLASH
- 1 MB SRAM
- 10/100/1000 Ethernet PHY
- IrDA
- USB 2.0
- PC Card
- Various surface mount footprints
- Power rail decoupling capacitors

**User Prototyping Module**
- Provides user prototyping interfaced to a single AvBus connector
- Prototyping grid area (0.1"")
- Various surface mount footprints
- Power rail decoupling capacitors
Audio/Video Module
ADS-AV-DAU | $300.00 USD resale
AvBus Adapter
ADS-AVBUS-ADAP | $35.00 USD resale
AvBus Breakout Module
ADS-AVBUS-DAU | $125.00 USD resale
Avnet Parallel Cable III
HW JTAG-PC | $95.00 USD resale
Communications/Memory Module
EDK | $250.00 USD resale
Communications/Memory Module
$499.00 USD resale
IBM 405 Evaluation Kit
ADS-IBM-405-EVL | $959.00 USD resale
IBM 440 Evaluation Kit
ADS-IBM-440-EVL | $749.00 USD resale
Motorola ColdFire Evaluation Kit
MOT-5282-EVL | $250.00 USD resale
Motorola ColdFire Evaluation Kit with P&E BDM interface
MOT-5282PE-EVL | $300.00 USD resale
Motorola PowerQUICC Evaluation Kit
MOT-857-EVL | $499.00 USD resale
Motorola 68HC08GP32 Evaluation Kit
MOT908GP32 | $149.95 USD resale
Motorola 68HC908GP32 emWare Enabled Development Kit
MOTGP32-EMW | $249.95 USD resale
Motorola M68HC08 Family Evaluation Kit, 908L3 Version
MOT-JL3-EVL | $89.95 USD resale
National Semiconductor Dual High-CURRENT Power Supply Kit
NSC-XP | $250.00 USD resale
Texas Instruments Analog Adapter Kit
TI-AD-DAU | $75.00 USD resale
Texas Instruments DSK to USB 2.0 Daughterboard
TI-USB-DAU | $99.00 USD resale
User Protootyping Module
PROTO-DAU | $95.00 USD resale
Xilinx & Cypress USB 2.0 to SCSI System Solution Kit
S2E-US2-SOL | $499.00 USD resale
Xilinx CoolRunner-II Evaluation Kit
XLX-CR2-EVL | $149.00 USD resale
ISE BaseX (only available with purchase of the above part number)
BASEX-BUNDLE | $550.00 USD resale
Xilinx PMC IRL Reference Design Kit
XLX-PMC-IRL | $2,495.00 USD resale
Xilinx Spartan-II Evaluation Kit
XLX-SP2-EVL | $149.00 USD resale
Xilinx Spartan-II Evaluation Kit
SP2-MB-EVL | $499.00 USD resale
ISE BaseX (only available with purchase of one the above part numbers)
BASEX-BUNDLE | $550.00 USD resale
Xilinx Spartan-IIIE Evaluation Kit
XLX-SP2E-EVL | $249.00 USD resale
Xilinx Spartan-IIIE Evaluation Kit
SP2E-MB-EVL | $650.00 USD resale
ISE BaseX (only available with purchase of one the above part numbers)
BASEX-BUNDLE | $550.00 USD resale
Xilinx Spartan-3 Evaluation Kit populated with an XC3S400 device
XLX-SP3-EVL400 | $399.00 USD resale
Xilinx Spartan-3 Evaluation Kit populated with an XC3S1500 device
XLX-SP3-EVL1500 | $499.00 USD resale
Xilinx Spartan-3 Evaluation Kit populated with an XC3S400 device ^1 & ^2
SP3-MB-EVL400 | $799.00 USD resale
Xilinx Spartan-3 Evaluation Kit populated with an XC3S1500 device ^1 & ^2
SP3-MB-EVL1500 | $899.00 USD resale
ISE BaseX (only available with purchase of the ADS-XLX-SP3-EVL400 part number)
BASEX-BUNDLE | $550.00 USD resale
ISE Foundation (only available with purchase of the ADS-XLX-SP3-EVL1500 part number)
FOUNDATION-BUNDLE | $2,400.00 USD resale
Xilinx Virtex-E Development Kit
XLX-VE-DEV | $1,495.00 USD resale
Xilinx Virtex-E Development Kit
VE-MB-DEV | $1,650.00 USD resale
Xilinx Virtex-E Evaluation Kit
XLX-VE-EVL | $149.00 USD resale
Xilinx Virtex-E Evaluation Kit
VE-MB-EVL | $499.00 USD resale
ISE BaseX (only available with purchase of one the above part numbers)
BASEX-BUNDLE | $550.00 USD resale
Xilinx Virtex-II Development Kit populated with an XC2V1500 device, - 6 speed grade
XLX-V2-DEV1500-6 | $1,200.00 USD resale
Xilinx Virtex-II Development Kit ^1 & ^2
XLX-MB-DEV1500-6 | $1,600.00 USD resale
Xilinx Virtex-II Development Kit with XC2V4000 and high-current power supply
XLX-V2-DEV4000XP | $3,000.00 USD resale
Xilinx Virtex-II Development Kit ^1 & ^2
V2-MB-DEV4000XP | $3,400.00 USD resale
Xilinx Virtex-II Development Kit with XC2V6000 and high-current power supply
XLX-V2-DEV6000XP | $6,000.00 USD resale
Xilinx Virtex-II Development Kit ^1 & ^2
V2-MB-DEV6000XP | $6,400.00 USD resale
ISE Foundation (only available with purchase of one the above part numbers)
FOUNDATION-BUNDLE | $2,400.00 USD resale
Xilinx Virtex-II Evaluation Kit
XLX-V2-EVL1000 | $400.00 USD resale
ISE Foundation (only available with purchase of the above part numbers)
FOUNDATION-BUNDLE | $2,400.00 USD resale
Xilinx Virtex-II High-Speed Evaluation Kit
XLX-V2X-EVL | $149.00 USD resale
Xilinx Virtex-II Pro Development Kit populated with XC2VP7, - 6 speed grade
XLX-V2PRO-DEVLP7-6 | $2,495.00 USD resale
Xilinx Virtex-II Pro Development Kit populated with XC2VP20, - 6 speed grade
XLX-V2PRO-DEVLP20-6 | $3,195.00 USD resale
Xilinx Virtex-II Pro Development Kit populated with XC2VP30, - 6 speed grade
XLX-V2PRO-DEVLP30-6 | $3,695.00 USD resale
ISE Foundation (only available with purchase of one the above part numbers)
FOUNDATION-BUNDLE | $2,400.00 USD resale
Xilinx Virtex-II Pro Evaluation Kit populated with XC2VP7, - 5 speed grade
XLX-V2PRO-EVL7-5 | $499.00 USD resale
Xilinx Virtex-II Pro Evaluation Kit populated with XC2VP7, - 6 speed grade
XLX-V2PRO-EVL8-5 | $599.00 USD resale
Xilinx Virtex-II Pro Evaluation Kit ^1-bundled with EDK and Linux BSP, - 5 speed grade
XLX-V2PRO-LX-EVL7-5 | $900.00 USD resale
Xilinx Virtex-II Pro Evaluation Kit ^1-bundled with EDK and Linux BSP, - 6 speed grade
XLX-V2PRO-LX-EVL8-5 | $1,000.00 USD resale
ISE Foundation (only available with purchase of one the above part numbers)
FOUNDATION-BUNDLE | $2,400.00 USD resale
Xilinx XPLA3 Evaluation Kit
XLX-X3-EVL | $149.00 USD resale

Footnote ^1 = bundled with Embedded Development Kit (EDK), Footnote ^2 = bundled with Communications/Memory Module